IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Byung-Sup Shim and Yo-Ho Kim

Serial No.

09/305,240

Examiner:

Nadav, Ori

Confirmation No.

1838

Filed:

May 4, 1999

Group Art Unit: 2811

For:

OPEN DRAIN INPUT/OUTPUT STRUCTURE AND

MANUFACTURING METHOD THEREOF IN SEMICONDUCTOR

DEVICE

Mail Stop AMENDMENT Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Responsive to the Office Action dated March 26, 2004, enclosed is an amendment in the above-identified application.

The fee has been calculated as shown below.

CLAIMS AS AMENDED					
For:	Number After Amendment	Previous Number	Extra	Rate	Additional Fee
Total Claims	25	16	90	x \$18 =	\$162
Independent Claims	5	5	0	x \$86 =	\$0
TOTAL ADDITIONAL FEE FOR THIS AMENDMENT					\$162

^{*}greater of twenty (20) or number for which fee has been paid

PTO Form 2038 authorizing credit card payment for the above-listed fees is enclosed. Any deficiency or overpayment should be charged or credited to deposit account number 13-1703.

Respectfully submitted,

MARGER JOHNSON & McCOLLOM, P.C.

Scott A. Schaffer Reg. No. 38,610

M DC

MARGER JOHNSON & McCOLLOM, P.C. 1030 SW Morrison Street Portland, OR 97205 (503) 222-3613

Customer No. 20575

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA

22313-1450 Date: June 24

/WY

Li Mei Vermilya

^{**}greater of three (3) or number for which fee has been paid



PATENT APPLICATION Docket No. 5484-048 Client No. PX828-US/SSD

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AMENDMENT

This paper is responsive to the non-final Office Action mailed 26 March 2004.

Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks/Arguments begin on page 8 of this paper.

An Appendix following page 16 of this paper contains the copyright page and pages 292-293 of Silicon Processing for the VLSI Era, Volume 3 – The Submicron MOSFET, by Stanley Wolf, Ph.D.

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